



PATENT

-1-

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Andrew Strachan et al.

Application No.: 10/659,421

Filed: September 10, 2003

For: METHOD OF IMPROVING THE
BREAKDOWN VOLTAGE OF A
DIFFUSED SEMICONDUCTOR
JUNCTION

Confirmation No.: 4626

Group Art Unit: 2812

Examiner: Stanetta D. Isaac

**RESPONSE TO FINAL REJECTION
MAILED DECEMBER 16, 2005**

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope, addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 10, 2006.

STALLMAN & POLLOCK LLP

Dated: 05/10/2006

By:

Tanisha Lawrence-Caceres

Sir:

In response to the Final Office Action mailed December 16, 2005, and in conjunction with Applicants' Request for Continuing Examination and Applicants' request for a two-month extension of time, please amend the above-identified application as follows:

Amendments to the claims are provided in the Listing of the Claims that begins on page 2 of this paper.

Remarks begin on page 5 of this paper.

Atty Docket No.: NSC1-M3200 [P05675]